

HPCPS-2015

4th IEEE International Workshop on High-Performance Chip, Package and Systems

Aug. 11, 2015; 2228 River Building, Carleton university, Ottawa, Canada



HPCPS is focused on the emerging high-performance electronic chip, packages and systems. Major theme of the workshop is focused on the co-design, modeling and analysis of the mixed-domain issues arising due to the high-speed, high-density and low-power requirements that are demanded from today's chip, interconnect, package and systems. Particular emphasis is also placed on signal and power integrity and electromagnetic compatibility in modern designs. Emerging techniques and methodologies for mixed-domain integration of diverse modules such as novel interconnects, optical components, RF devices and MEMS with electronic chips and packages are also considered.

This year's program features 5 eminent researchers and leaders in the area, including, Keynote Speeches, Invited Talks and Distinguished Lectures from:

[Prof. Subramanian Iyer](#), Distinguished Chancellor's Professor, Dept. of EE, UCLA, Los Angeles

[Antoine Paquin](#), President and CEO, Solantro Semiconductor Corp., Ottawa, ON, Canada

[Dr. Peter Thoma](#), Managing Director, R&D, CST GmbH, Damstadt, Germany

[Prof. John Long](#), Professor, Dept. of ECE, University of Waterloo, Dept. of ECE, Waterloo, ON, Canada

[Prof. Ricardo Reis](#), Professor, UFRGS, Porto Alegre, Brazil

HPCPS is pleased give **20 student awards in the form of free/discounted registrations** (depending on IEEE/CASS/SSCS Memberships) on a **first-come-first-serve basis** (depending on the IEEE Membership) to the workshop. To be considered for the award, please fill up this [Application Form](#) and submit it by email by 3rd Nov. 2015 to admin@hpcps.org.

HPCPS-2015 is supported by the IEEE Circuits & Systems Society, IEEE Solid state Circuits Society, IEEE Ottawa Section, IEEE Canada Eastern Area, several local chapters of the IEEE and Dept. of Electronics, Carleton University.

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